

 Active

- L1: (4279) (DLL or (delay adj locked adj loop))
- L3: (2143) 1 and (clocks or (clock adj signals))
- L4: (548) 3 and (phase near detect\$3)
- L6: (223) 4 and counter
- L7: (8) 6 and (gray adj code)
- L8: (154) counter and (gray adj code)
- L9: (4) 8 and upward and downward
- L10: (4) 9 and generat\$3
- L11: (4) 8 and (DLL or (delay adj locked adj loop))

 Failed

Search

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Queue

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DBs

USPAT; US-PGPUB; EPO; JPO; DERWENT

 Plurals

Default operator:

OR

 Highlight all hit terms initially

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 BRS term IsR... Image Text HTML

#	Type	L #	Hits	Search Text	DBs	Time Stamp	C	E	Ext
1	BRS	L1	4279	(DLL or (delay adj locked adj loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 10:52			0
2	BRS	L3	2143	1 and (clocks or (clock adj signals))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:00			0
3	BRS	L4	548	3 and (phase near detect\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:01			0
4	BRS	L6	223	4 and counter	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:02			0
5	BRS	L7	8	6 and (gray adj code)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:10			0
6	BRS	L8	154	counter and (gray adj code) and register and converter and binary and multiplexer	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:16			0
7	BRS	L9	4	8 and upward and downward	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:19			0
8	BRS	L10	4	9 and generat\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:19			0
9	BRS	L11	4	8 and (DLL or (delay adj locked adj loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2002/05/23 11:25			0

 Hits Details HTML